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Application Number	10612061
Filing Date	2003-06-30
First Named Inventor	Eric L. Debes
Art Unit	2193
Examiner Name	Malzahn, David H.
Attorney Docket Number	42P15765

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2	20020112147	2002-08-15	Chennupaty, Srinivas; et al.
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6	CASE, B., "Philips Hopes to Displace DSPs with VLIW, TriMedia Processors Aimed at Future Multimedia Embedded Apps," Microprocessor Report, December 1994, pp. 12-18	<input type="checkbox"/>
7	Gwennap, L., "New PA-RISC Processor Decodes MPEG Video, H's PA-7100LC Uses New Instructions to Eliminate Decoder Chip," Microprocessor Report, January, 1994, pp. 16-17	<input type="checkbox"/>
8	TEXAS INSTRUMENTS, "TMS320C2X User's Guide," 1993, pp 3:2-3:11; 3:28-3:34; 4:1-4:22; 4:41; 4:103; 4:119-J;120; 4:122; 4:150-4:151	<input type="checkbox"/>
9	INTEL CORPORATION, "i860 TM Microprocessor Family Programmer's Reference Manual," 1992, Chapters 1, 3, 8 and 11	<input type="checkbox"/>
10	LEE, R. B., "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, April 1995, pp. 22-32	<input type="checkbox"/>
11	INTEL CORPORATION, "Pentium Processor's User's Manual, vol. 3; Architecture and Programming Manual," 1993, Chapters 1, 3, 4, 6, 8, and 18	<input type="checkbox"/>
12	MARGULIS, N., "i860 Microprocessor Architecture," McGraw Hill, Inc., 1990, Chapters 6, 7, 8, 10, and 11	<input type="checkbox"/>
13	INTEL CORPORATION, "Intel i750, i860 TM, i960 Processors and Related Products, 1993, pp. 1-3	<input type="checkbox"/>
14	MOTOROLA, INC., "Motorola MC88110 Second Generation RISC Microprocessor User's Manual," 1991	<input type="checkbox"/>
15	MOTOROLA, INC., "Errata to MC88110 Second Generation RISC Microprocessor User's Manual," 1992, pp. 1-11	<input type="checkbox"/>
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17	SHIPNES, J., "Graphics Processing with the 88110 RISC Microprocessor," Motorola, Inc. IEEE, No. 0-8186-26455-0/92, 1992, pp. 169-174	<input type="checkbox"/>
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19	HANSEN, C., "Architecture of a Broadband Mediaprocessor," Proceedings of Compcon, IEEE, 1996, pp. 334-340	<input type="checkbox"/>
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21	LEVINTHAL, ET AL., "Parallel Computers for Graphics Applications," Proceedings: Second International Conference on Architectural Support for Programming Languages and Operating Systems, (ASPLOS II), IEEE, 1987, pp. 193-198	<input type="checkbox"/>
22	WANG, ET AL., "A Processor Architecture for 3D Graphics Calculations," Computer Motion, Inc., Goleta, CA, 23 pgs	<input type="checkbox"/>

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